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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

TI-30481

Gary L. Swoboda, et al.

Art Unit: 2128

Serial No.: 09/943,599

Examiner: Akash Saxena

Filed: August 30, 2001

Conf. No.: 2479

For: Correlating On-Chip Data Processor Trace Information for Export

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Robin E. Barnum

December 9, 2005  
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**FACSIMILE COVER SHEET**

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<input type="checkbox"/> <b>DIVISIONAL APP'N</b>	
<b>NAME OF INVENTOR(S):</b> Gary L. Swoboda, et al.	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.: 09/943,599</b> <b>Filing Date: August 30, 2001</b>	
<b>TITLE OF INVENTION:</b> Correlating On-Chip Data Processor Trace Information for Export	
<b>TI FILE NO.:</b> <b>TI-30481</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>
<b>FAXED:</b> 12/9/05 <b>DUE:</b> 12/9/05 <b>ATTY/SEC'Y:</b> RDM/reb	

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DEC 09 2005

RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2128

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


Applicant: Swoboda et al                      Art Unit: 2128  
Serial No.: 09/943,599                      Examiner: Akash Saxena  
Filed: August 30, 2001                      Docket: TI-30481  
For: CORRELATING ON-CHIP DATA PROCESSOR TRACE INFORMATION FOR  
EXPORT

AMENDMENT UNDER 37 C.F.R. 1.116

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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UNDER 37 C.F.R. §1.6(b)

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Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated September 9, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: